

1/2

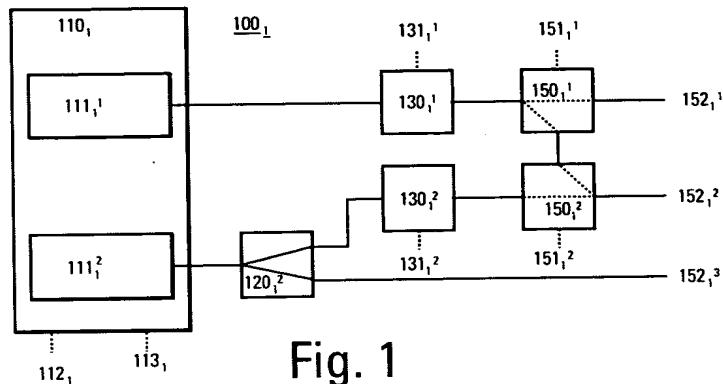


Fig. 1

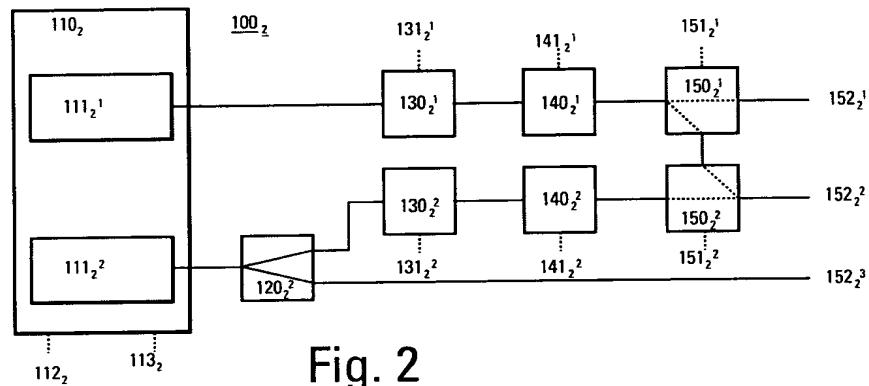


Fig. 2

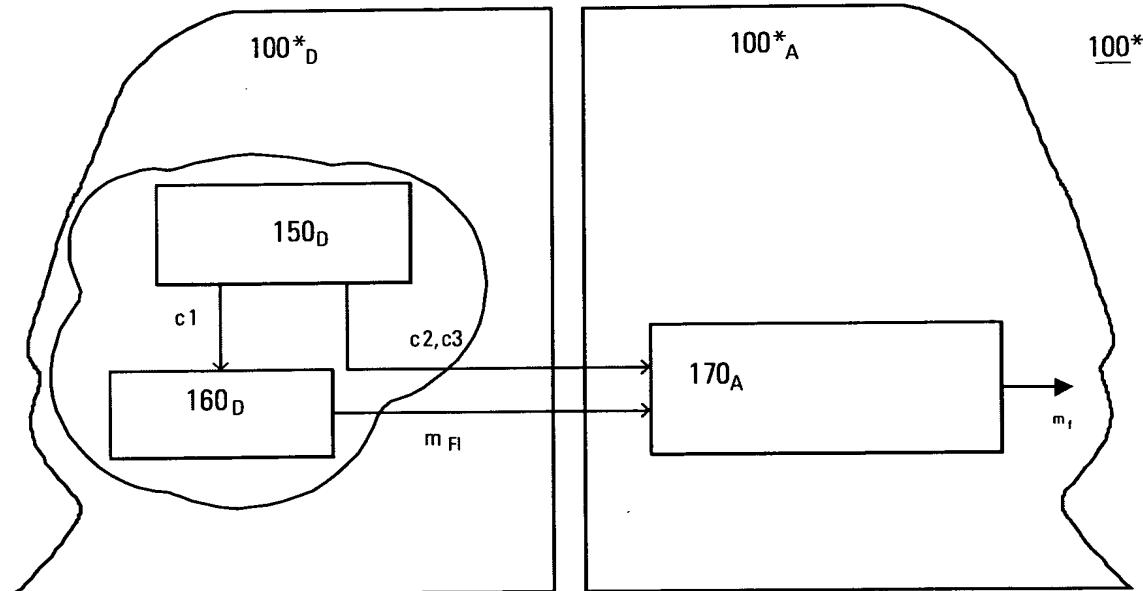


Fig. 3

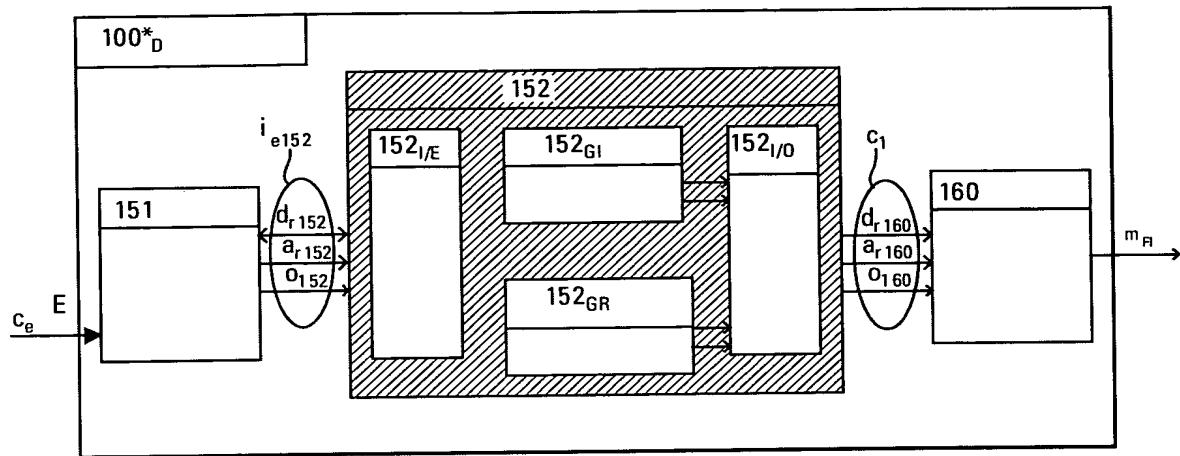


Fig. 4a

ADDR	MODE	REGISTER	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
4000104h	R/W	interrogation_1																																		
4000150h	WO	Burst Trigger																																		
4000240h	R/W	Configuration 153																																		

Fig. 4b